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Should you have any question or inquiry on this matter, please contact our sales staff.

積層パワーチョークコイル MULTILAYER CHIP INDUCTORS (FOR POWER SUPPLY LINES) CK SERIES P TYPE



OPERATING TEMP.	-40~85°C
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特長 FEATURES

- ・高さ1.2mm未満のコンパクトサイズ。
- ・内部電極の印刷技術及びセラミックスグリーンシート技術の向上により、従来の内部電極よりも高アスペクト比のAg厚膜を形成し、大幅な低Rdc化を実現。
- ・ Low profile below 1.2mm.
- ・ Low DC power dissipation due to Low Rdc with High Aspect Ratio internal conductor that stands on the Green Sheet and Printing technologies.

用途 APPLICATIONS

- ・携帯電話、DSC、DVC等の携帯小型電子機器のDC-DCコンバータ
- ・ DC/DC converter for the Mobile equipments; Cellular Phones, DSC, DVC.

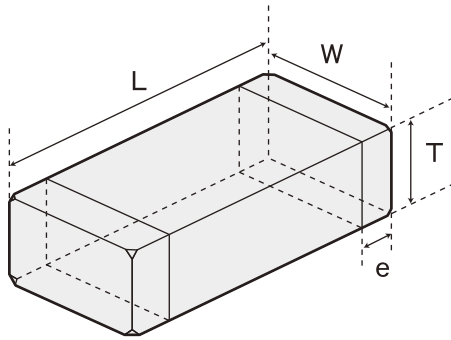
形名表記法 ORDERING CODE

<p>1</p> <table border="1"> <tr><td>形式</td></tr> <tr><td>CKP 積層パワーチョークコイル</td></tr> </table> <p>2</p> <table border="1"> <tr><td>形状寸法 (L×W) (mm)</td></tr> <tr><td>2520 (1008) 2.5×2.0</td></tr> </table> <p>3</p> <table border="1"> <tr><td>製品厚み (mm)</td></tr> <tr><td>V 1.2Max</td></tr> <tr><td>△ 1.0Max</td></tr> <tr><td>△=スペース</td></tr> </table>	形式	CKP 積層パワーチョークコイル	形状寸法 (L×W) (mm)	2520 (1008) 2.5×2.0	製品厚み (mm)	V 1.2Max	△ 1.0Max	△=スペース	<p>4</p> <table border="1"> <tr><td>公称インダクタンス [μH]</td></tr> <tr><td>例</td></tr> <tr><td>1R0 1.0</td></tr> <tr><td>※R=小数点</td></tr> </table>	公称インダクタンス [μH]	例	1R0 1.0	※R=小数点	<p>5</p> <table border="1"> <tr><td>インダクタンス許容差 [%]</td></tr> <tr><td>M ±20</td></tr> </table> <p>6</p> <table border="1"> <tr><td>包装</td></tr> <tr><td>-T リールテーピング</td></tr> </table>	インダクタンス許容差 [%]	M ±20	包装	-T リールテーピング	<p>7</p> <table border="1"> <tr><td>当社管理記号</td></tr> <tr><td>△ 標準品</td></tr> <tr><td>△=スペース</td></tr> </table>	当社管理記号	△ 標準品	△=スペース
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外形寸法 EXTERNAL DIMENSIONS



Type	L	W	T	e
CKP2520 (1008)	2.5±0.2 (0.098±0.008)	2.0±0.2 (0.079±0.008)	1.0max (0.039max)	0.5±0.3 (0.02±0.012)
			1.2max (0.047max)	

Unit : mm (inch)

概略バリエーション AVAILABLE INDUCTANCE RANGE

Range	Type	CKP2520	Idc1 [A]	Idc2 [A]
Inductance [μH]	1.0	1R0M	0.80	1.4
	1.5	1R5M	0.60	1.3
	2.2	2R2M	0.40	1.3
	3.3	3R3M	0.15	1.2
	4.7	4R7M	0.15	1.1
	1.0	V1R0M	1.5	1.2
	2.2	V2R2M	0.7	1.1
	3.3	V3R3M	0.4	1.1

※) Idc1は、直流重畳によるインダクタンス低下が30%以内となる直流電流値 (at 20°C)

※) Idc1 is the DC current value having inductance decrease down to 30%. (at 20°C)

※) Idc2は、温度上昇が40°Cとなる直流電流値 (at 20°C)

※) Idc2 is the DC current value having temperature increase up to 40°C. (at 20°C)

セレクトションガイド
Selection Guide



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etc

アイテム一覧
Part Numbers



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特性図
Electrical Characteristics



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梱包
Packaging



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信頼性
Reliability Data



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使用上の注意
Precautions



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CKP2520

形名 Ordering code	EHS (Environmental Hazardous Substances)	公称 インダクタンス Inductance [μH]	インダクタンス 許容差 Inductance tolerance	直流抵抗 DC resistance [Ω] (max.)	定格電流 ※) Rated current [A] (max.)		測定周波数 Measuring frequency [MHz]	厚み Thickness [mm] (inch)
					直流重量許容電流 Saturation current Idc1	温度上昇許容電流 Temperature rise current Idc2		
CKP2520 1R0M	RoHS	1.0	±20%	0.08	0.80	1.4	1	1.0max (0.039max)
CKP2520 1R5M	RoHS	1.5		0.09	0.60	1.3		
CKP2520 2R2M	RoHS	2.2		0.09	0.40	1.3		
CKP2520 3R3M	RoHS	3.3		0.12	0.15	1.2		
CKP2520 4R7M	RoHS	4.7		0.15	0.15	1.1		
CKP2520V1R0M	RoHS	1.0		0.12	1.50	1.2		1.2max (0.047max)
CKP2520V2R2M	RoHS	2.2		0.15	0.70	1.1		
CKP2520V3R3M	RoHS	3.3		0.15	0.40	1.1		

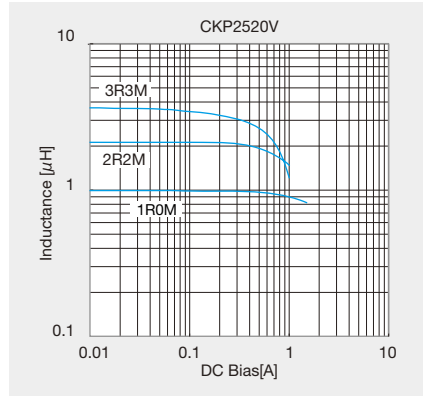
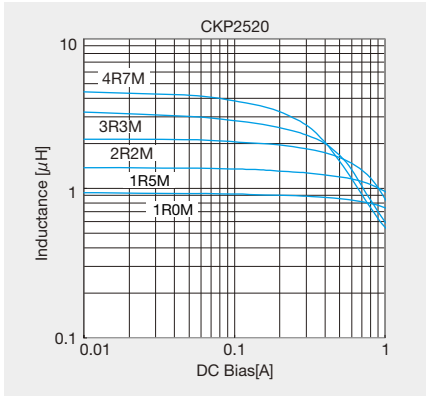
※) Idc1は、直流重量によるインダクタンス低下が30%以内となる直流電流値 (at 20°C)

※) Idc1 is the DC current value having inductance decrease down to 30%. (at 20°C)

※) Idc2は、温度上昇が40°Cとなる直流電流値 (at 20°C)

※) Idc2 is the DC current value having temperature increase up to 40°C. (at 20°C)

直流重量特性例 DC Bias characteristics



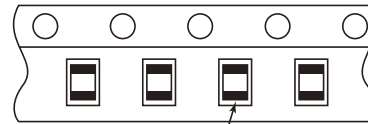
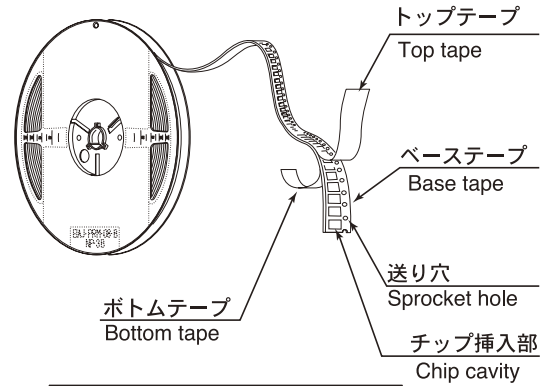
①最小受注単位数 Minimum Quantity
 ■テーピング梱包 Tape & Reel Packaging

形式 Type	製品厚み Thickness [mm] (inch)	標準数量 [pcs] Standard Quantity	
		紙テープ Paper Tape	エンボステープ Embossed Tape
CK1608(0603)	0.8 (0.031)	4000	—
CK2125(0805)	0.85 (0.033)	4000	—
	1.25 (0.049)	—	2000
CKP2520(1008)	0.9 (0.035)	—	3000
	1.1 (0.043)	—	2000
LK1005(0402)	0.5 (0.020)	10000	—
LK1608(0603)	0.8 (0.031)	4000	—
LK2125(0805)	0.85 (0.033)	4000	—
	1.25 (0.049)	—	2000
HK0603(0201)	0.3 (0.012)	15000	—
HK1005(0402)	0.5 (0.020)	10000	—
HK1608(0603)	0.8 (0.031)	4000	—
HK2125(0805)	0.85 (0.033)	—	4000
	1.0 (0.039)	—	3000
HKQ0603S(0201)	0.3 (0.012)	15000	—
AQ105(0402)	0.5 (0.020)	10000	—
BK0603(0201)	0.3 (0.012)	15000	—
BK1005(0402)	0.5 (0.020)	10000	—
BK1608(0603)	0.8 (0.031)	4000	—
BK2125(0805)	0.85 (0.033)	4000	—
	1.25 (0.049)	—	2000
BK2010(0804)	0.45 (0.018)	4000	—
BK3216(1206)	0.8 (0.031)	—	4000
BKP0603(0201)	0.3 (0.012)	15000	—
BKP1005(0402)	0.5 (0.020)	10000	—
BKP1608(0603)	0.8 (0.031)	4000	—
BKP2125(0805)	0.85 (0.033)	4000	—

②テーピング材質 Taping material

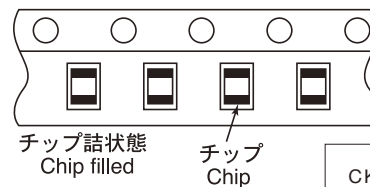
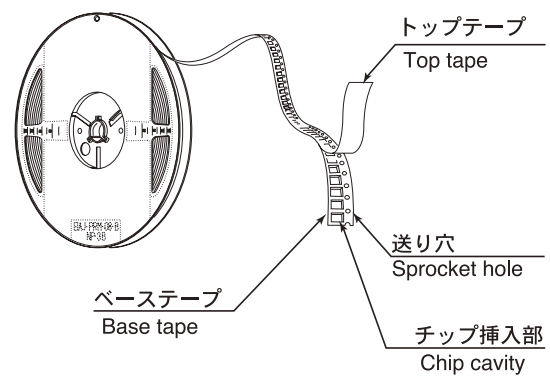
紙テープ

Card board carrier tape



CK	1 6 0 8
CK	2 1 2 5
LK	1 0 0 5
LK	1 6 0 8
LK	2 1 2 5
HK	0 6 0 3
HK	1 0 0 5
HK	1 6 0 8
HK Q	0 6 0 3
AQ	1 0 5
BK	0 6 0 3
BK	1 0 0 5
BK	1 6 0 8
BK	2 1 2 5
BK	2 0 1 0
BK P	0 6 0 3
BK P	1 0 0 5
BK P	1 6 0 8
BK P	2 1 2 5

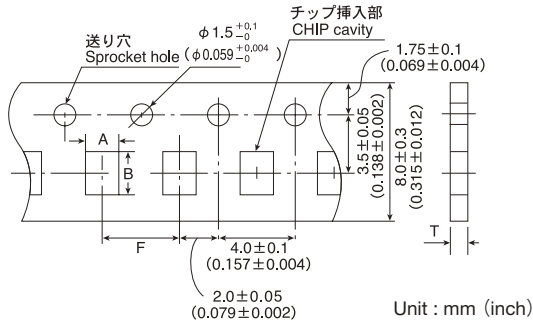
エンボステープ
Embossed Tape



CK	2125
CKP	2520
LK	2125
HK	2125
BK	2125
BK	3216

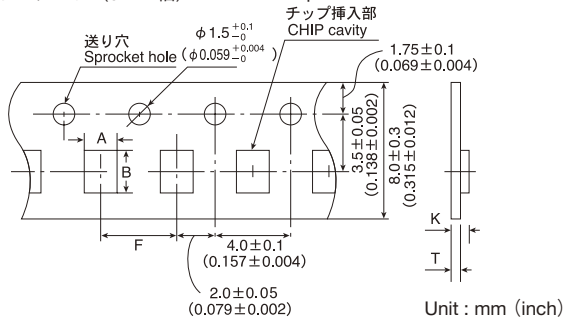
③テーピング寸法 Taping Dimensions

・紙テープ (8mm幅) Paper tape (0.315 inches wide)



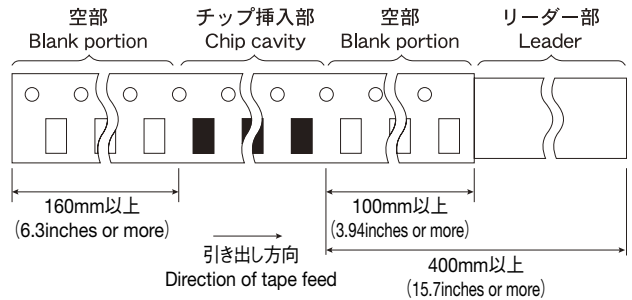
形式 Type	製品厚み Thickness (mm) (inch)	チップ挿入部 Chip cavity		挿入ピッチ Insertion Pitch F	テープ厚み Tape Thickness T	
		A	B		T	
CK1608(0603)	0.8 (0.031)	1.0±0.2 (0.039±0.008)	1.8±0.2 (0.071±0.008)	4.0±0.1 (0.157±0.004)	1.1max (0.043max)	
CK2125(0805)	0.85 (0.033)	1.5±0.2 (0.059±0.008)	2.3±0.2 (0.091±0.008)	4.0±0.1 (0.157±0.004)	1.1max (0.043max)	
LK1005(0402)	0.5 (0.020)	0.65±0.1 (0.026±0.004)	1.15±0.1 (0.045±0.004)	2.0±0.05 (0.079±0.002)	0.8max (0.031max)	
LK1608(0603)	0.8 (0.031)	1.0±0.2 (0.039±0.008)	1.8±0.2 (0.071±0.008)	4.0±0.1 (0.157±0.004)	1.1max (0.043max)	
LK2125(0805)	0.85 (0.033)	1.5±0.2 (0.059±0.008)	2.3±0.2 (0.091±0.008)	4.0±0.1 (0.157±0.004)	1.1max (0.043max)	
HK0603(0201)	0.3 (0.012)	0.40±0.06 (0.016±0.002)	0.70±0.06 (0.028±0.002)	2.0±0.05 (0.079±0.002)	0.45max (0.018max)	
HK1005(0402)	0.5 (0.020)	0.65±0.1 (0.026±0.004)	1.15±0.1 (0.045±0.004)	2.0±0.05 (0.079±0.002)	0.8max (0.031max)	
HK1608(0603)	0.8 (0.031)	1.0±0.2 (0.039±0.008)	1.8±0.2 (0.071±0.008)	4.0±0.1 (0.157±0.004)	1.1max (0.043max)	
HKQ0603S(0201)	0.3 (0.012)	0.40±0.06 (0.016±0.002)	0.70±0.06 (0.028±0.002)	2.0±0.05 (0.079±0.002)	0.45max (0.018max)	
AQ105(0402)	0.5 (0.020)	0.75±0.1 (0.030±0.004)	1.15±0.1 (0.045±0.004)	2.0±0.05 (0.079±0.002)	0.8max (0.031max)	
BK0603(0201)	0.3 (0.012)	0.40±0.06 (0.016±0.002)	0.70±0.06 (0.028±0.002)	2.0±0.05 (0.079±0.002)	0.45max (0.018max)	
BK1005(0402)	0.5 (0.020)	0.65±0.1 (0.026±0.004)	1.15±0.1 (0.045±0.004)	2.0±0.05 (0.079±0.002)	0.8max (0.031max)	
BK1608(0603)	0.8 (0.031)	1.0±0.2 (0.039±0.008)	1.8±0.2 (0.071±0.008)	4.0±0.1 (0.157±0.004)	1.1max (0.043max)	
BK2125(0805)	0.85 (0.033)	1.5±0.2 (0.059±0.008)	2.3±0.2 (0.091±0.008)	4.0±0.1 (0.157±0.004)	1.1max (0.043max)	
BK2010(0804)	0.45 (0.018)	1.2±0.1 (0.047±0.004)	2.17±0.1 (0.085±0.004)	4.0±0.1 (0.157±0.004)	0.8max (0.031max)	
BKP0603(0201)	0.3 (0.012)	0.40±0.06 (0.016±0.002)	0.70±0.06 (0.028±0.002)	2.0±0.05 (0.079±0.002)	0.45max (0.018max)	
BKP1005(0402)	0.5 (0.020)	0.65±0.1 (0.026±0.004)	1.15±0.1 (0.045±0.004)	2.0±0.05 (0.079±0.002)	0.8max (0.031max)	
BKP1608(0603)	0.8 (0.031)	1.0±0.2 (0.039±0.008)	1.8±0.2 (0.071±0.008)	4.0±0.1 (0.157±0.004)	1.1max (0.043max)	
BKP2125(0805)	0.85 (0.033)	1.5±0.2 (0.059±0.008)	2.3±0.2 (0.091±0.008)	4.0±0.1 (0.157±0.004)	1.1max (0.043max)	

・エンボステープ (8mm幅) Embossed Tape (0.312 inches wide)

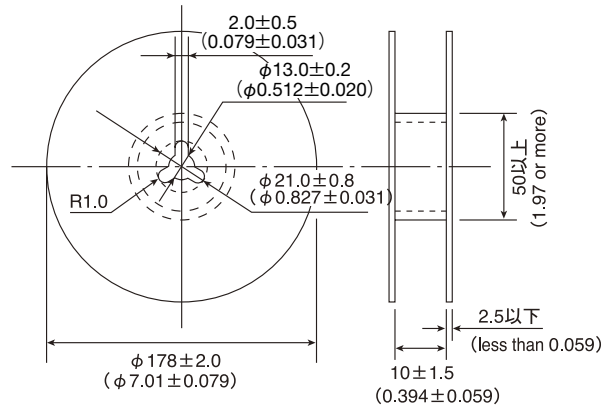


形式 Type	製品厚み Thickness (mm) (inch)	チップ挿入部 Chip cavity		挿入ピッチ Insertion Pitch F	テープ厚み Tape Thickness	
		A	B		K	T
CK2125(0805)	1.25 (0.049)	1.5±0.2 (0.059±0.008)	2.3±0.2 (0.091±0.008)	4.0±0.1 (0.157±0.004)	2.0 (0.079)	0.3 (0.012)
CKP2520(1008)	0.9 (0.035)	2.3±0.1 (0.091±0.004)	2.8±0.1 (0.110±0.004)	4.0±0.1 (0.157±0.004)	1.4 (0.055)	0.3 (0.012)
	1.1 (0.043)				1.7 (0.067)	
LK2125(0805)	1.25 (0.049)	1.5±0.2 (0.059±0.008)	2.3±0.2 (0.091±0.008)	4.0±0.1 (0.157±0.004)	2.0 (0.079)	0.3 (0.012)
HK2125(0805)	0.85 (0.033)	1.5±0.2 (0.059±0.008)	2.3±0.2 (0.091±0.008)	4.0±0.1 (0.157±0.004)	1.5 (0.059)	0.3 (0.012)
	1.0 (0.039)				2.0 (0.079)	
BK2125(0805)	1.25 (0.049)	1.5±0.2 (0.059±0.008)	2.3±0.2 (0.091±0.008)	4.0±0.1 (0.157±0.004)	2.0 (0.079)	0.3 (0.012)
BK3216(1206)	0.8 (0.031)	1.9±0.1 (0.075±0.004)	3.5±0.1 (0.138±0.004)	4.0±0.1 (0.157±0.004)	1.4 (0.055)	0.3 (0.012)

④リーダー部・空部 LEADER AND BLANK PORTION

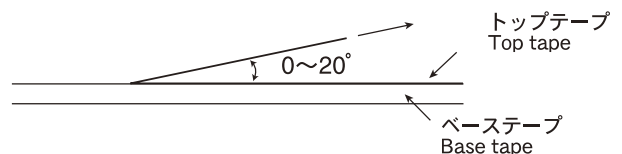


⑤リール寸法 Reel Size

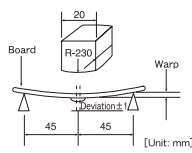


⑥トップテープ強度 Top tape strength

トップテープの剥離力は、下図矢印方向にて0.1~0.7Nとなります。The top tape requires a peel-off force of 0.1~0.7N in the direction of the arrow as illustrated below.



Multilayer chip inductors and beads

Item	Specified Value																				Test Methods and Remarks			
	BK0603	BK1005	BK1608	BK2125	ARRAY		BKP0603	BKP1005	BKP1608	BKP2125	CK1608	CK2125	CKP2520	LK1005	LK1608	LK2125	HK0603	HK1005	HK1608	HK2125		HKQ0603S	AQ105	
					BK2010	BK3216																		
6. Q																								CK Series : Measuring frequency : 2 to 4MHz (CK1608) Measuring frequency : 2 to 25MHz (CK2125) LK Series : Measuring frequency : 10 to 25MHz (LK1005) Measuring frequency : 1 to 50MHz (LK1608) Measuring frequency : 0.4 to 50MHz (LK2125) Measuring equipment, jig : HP4194 + 16085B + 16092A (or its equivalent) · HP4195A + 41951 + 16092A (or its equivalent) · HP4294A + 16192A · HP4291A + 16193A (LK1005) Measuring current : · 1mA rms (0.047 to 4.7μH) · 0.1mA rms (5.6 to 33μH) HK, HKQ, AQ Series : Measuring frequency : 100MHz (HK0603 · HK1005 · AQ105) Measuring frequency : 50/100MHz (HK1608 · HK2125) Measuring frequency : 500MHz (HKQ0603S) Measuring equipment, jig : · HP4291A + 16197A (HK0603 · AQ105) · HP4291A + 16193A (HK1005) · E4991A + 16197A (HKQ0603S) · HP4294A + 16092A+ in-house made jig (HK1608 · HK2125)
7. DC Resistance	0.07~ 1.50Ω max.	0.05~ 0.80Ω max.	0.05~ 1.10Ω max.	0.05~ 0.75Ω max.	0.10~ 0.90Ω max.	0.15~ 0.80Ω max.	0.065~ 0.070Ω max.	0.140Ω max.	0.025~ 0.140Ω max.	0.020~ 0.050Ω max.	0.45~ 0.85Ω (±30%)	0.16~ 0.65Ω max.	0.08~ 0.15 max.	0.7~ 1.70Ω max.	0.2~ 2.2Ω max.	0.1~ 1.1Ω max.	0.11~ 3.74Ω max.	0.08~ 4.8Ω max.	0.05~ 2.6Ω max.	0.10~ 1.5Ω max.	0.06~ 1.29Ω max.	0.07~ 0.45Ω max.	Measuring equipment : VOAC-7412 (made by Iwasaki Tsushinki) VOAC-7512 (made by Iwasaki Tsushinki)	
8. Self Resonance Frequency (SRF)																								LK Series : Measuring equipment : HP4195A Measuring jig : 41951+16092A (or its equivalent) HK, HKQ, AQ Series : Measuring equipment : HP8719C HP8753D (HK2125)
9. Temperature Characteristic																								Inductance change : Within ±10% HK, HKQ, AQ Series : Temperature range : -30 to +85°C Reference temperature : +20°C
10. Resistance to Flexure of Substrate	No mechanical damage.																				Warp : 2mm Testing board : glass epoxy-resin substrate Thickness : 0.8mm 			

Multilayer chip inductors and beads

Item	Specified Value																			Test Methods and Remarks	
	BK0603	BK1005	BK1608	BK2125	ARRAY		BKP0603	BKP1005	BKP1608	BKP2125	CK1608	CK2125	CKP2520	LK1005	LK1608	LK2125	HK0603	HK1005	HK1608		HK2125
11. Solderability	At least 75% of terminal electrode is covered by new solder.										At least 75% of terminal electrode is covered by new solder.										Solder temperature : 230±5°C Duration : 4±1 sec.
12. Resistance to Soldering	Appearance : No significant abnormality. Impedance change : Within ±30%										No mechanical damage. Remaining terminal electrode : 70% min. Inductance change R10~4R7 : Within±10% 6R8~100 : Within±15% CKP2520 : Within±30%	No mechanical damage. Remaining terminal electrode : 70% min. Inductance change 47N~4R7 : Within±10% 5R6~330 : Within±15%	No mechanical damage. Remaining terminal electrode : 70% min. Inductance change 47N~4R7 : Within±10% 5R6~330 : Within±15%	No mechanical damage. Remaining terminal electrode : 70% min. Inductance change Within ±5%	Solder temperature : 260±5°C Duration : 10±0.5 sec. Preheating temperature : 150 to 180°C Preheating time : 3 min. Flux : Immersion into methanol solution with colophony for 3 to 5 sec. Recovery : 2 to 3 hrs of recovery under the standard condition after the test. (See Note 1)						
13. Thermal Shock	Appearance : No significant abnormality. Impedance change : Within ±30%										No mechanical damage. Inductance change : Within ±20% Qchange : Within ±30%	No mechanical damage. Inductance change : Within ±20% Qchange : Within ±30%	No mechanical damage. Inductance change : Within ±10% Qchange : Within ±20%	No mechanical damage. Inductance change : Within ±10% Qchange : Within ±20%	Conditions for 1 cycle Step 1: Minimum operating temperature +0 ~ -3 °C 30±3 min. Step 2: Room temperature 2 to 3 min. Step 3: Maximum operating temperature +0 ~ -3 °C 30±3 min. Step 4: Room temperature 2 to 3 min. Number of cycles: 5 Recovery : 2 to 3 hrs of recovery under the standard condition after the test. (See Note 1)						

(Note 1) When there are questions concerning measurement result ; measurement shall be made after 48 ± 2 hrs of recovery under the standard condition.

Multilayer chip inductors and beads

Item	Specified Value																			Test Methods and Remarks			
	BK0603	BK1005	BK1608	BK2125	ARRAY		BKP0603	BKP1005	BKP1608	BKP2125	CK1608	CK2125	CKP2520	LK1005	LK1608	LK2125	HK0603	HK1005	HK1608		HK2125	HKQ0603S	AQ105
					BK2010	BK3216																	
14. Damp Heat (Steady state)	Appearance : No significant abnormality. Impedance change : Within $\pm 30\%$											No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage. Inductance change : Within $\pm 10\%$ Q change : Within $\pm 20\%$					BBK Series : Temperature : $40 \pm 2^\circ\text{C}$ Humidity : 90 to 95%RH Duration : $500 \begin{smallmatrix} +24 \\ -0 \end{smallmatrix}$ hrs Recovery : 2 to 3 hrs of recovery under the standard condition after the removal from test chamber. (See Note 1) LK, CK, CKP, HK, HKQ, AQ Series : Temperature : $40 \pm 2^\circ\text{C}$ (LK, CK, CKP Series) : $60 \pm 2^\circ\text{C}$ (HK, HKQ, AQ Series) Humidity : 90 to 95%RH Duration : 500 ± 12 hrs Recovery : 2 to 3 hrs of recovery under the standard condition after the removal from test chamber. (See Note 1)
15. Loading under Damp Heat	Appearance : No significant abnormality. Impedance change : Within $\pm 30\%$											No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage. Inductance change : Within $\pm 10\%$ Q change : Within $\pm 20\%$					BK Series : Temperature : $40 \pm 2^\circ\text{C}$ Humidity : 90 to 95%RH Duration : $500 \begin{smallmatrix} +24 \\ -0 \end{smallmatrix}$ hrs Recovery : 2 to 3 hrs of recovery under the standard condition after the removal from test chamber. (See Note 1) LK, CK, CKP, HK, HKQ, AQ Series : Temperature : $40 \pm 2^\circ\text{C}$ (LK, CK, CKP Series) : $60 \pm 2^\circ\text{C}$ (HK, HKQ, AQ Series) Humidity : 90 to 95%RH Duration : 500 ± 12 hrs Recovery : 2 to 3 hrs of recovery under the standard condition after the removal from test chamber. (See Note 1)
16. Loading at High Temperature	Appearance : No significant abnormality. Impedance change : Within $\pm 30\%$											No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage.	No mechanical damage. Inductance change : Within $\pm 10\%$ Q change : Within $\pm 20\%$					BK Series : Temperature : $125 \pm 3^\circ\text{C}$ Applied current : Rated current Duration : $500 \begin{smallmatrix} +24 \\ -0 \end{smallmatrix}$ hrs Recovery : 2 to 3 hrs of recovery under the standard condition after the removal from test chamber. (See Note 1) LK, CK, CKP, HK, HKQ, AQ Series, BK Series P type : Temperature : $85 \pm 2^\circ\text{C}$ (LK, CK, CKP Series) : $85 \pm 3^\circ\text{C}$ (BK Series P type) : $85 \pm 2^\circ\text{C}$ (HK1608, 2125) : $85 \pm 2^\circ\text{C}$ (HK1005, AQ105 operating temperature range -55 to +85°C) : $125 \pm 2^\circ\text{C}$ (HK0603, HK1005, HKQ0603S, AQ105 operating temperature range -55 to +125°C) Applied current : Rated current Duration : 500 ± 12 hrs Recovery : 2 to 3 hrs of recovery under the standard condition after the test. (See Note 1)

Note on standard condition: "standard condition" referred to herein is defined as follows:
5 to 35°C of temperature, 45 to 85% relative humidity, and 86 to 106kPa of air pressure.

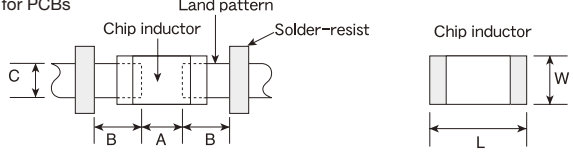
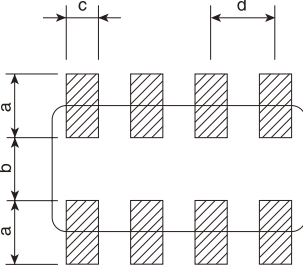
When there are questions concerning measurement results:

In order to provide correlation data, the test shall be conducted under condition of $20 \pm 2^\circ\text{C}$ of temperature, 60 to 70% relative humidity, and 86 to 106kPa of air pressure. Unless otherwise specified, all the tests are conducted under the "standard condition."

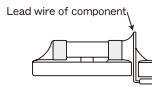
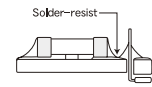
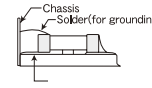
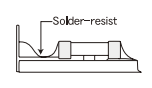
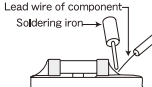
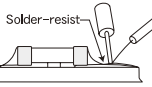
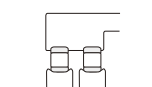
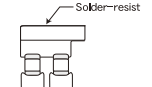
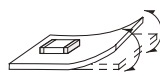
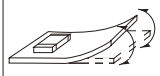
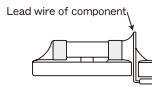
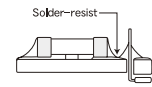
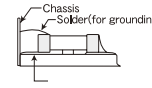
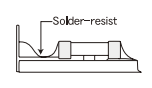
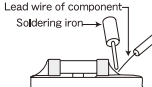
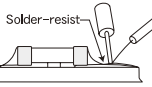
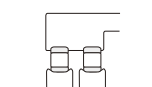
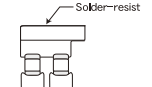
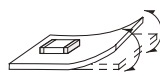
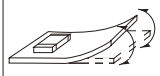
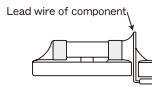
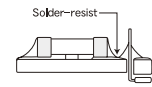
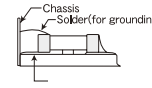
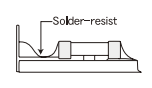
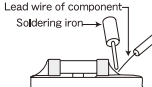
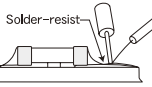
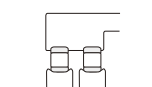
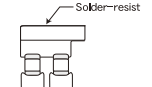
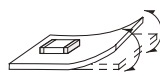
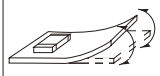
(Note 1)

measurement shall be made after 48 ± 2 hrs of recovery under the standard condition.

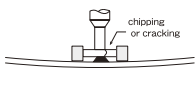
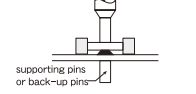
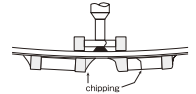
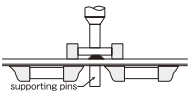
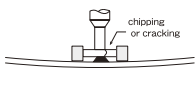
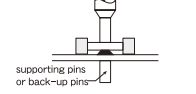
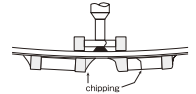
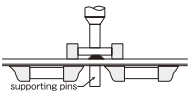
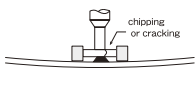
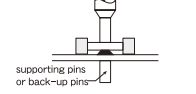
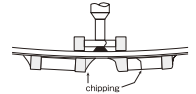
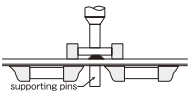
Precautions on the use of Multilayer chip Inductors, Multilayer chip inductors for high frequency, Multilayer ferrite chip beads

Stages	Precautions	Technical considerations																																																																																																					
1. Circuit Design	<p>◆Verification of operating environment, electrical rating and performance</p> <p>1. A malfunction in medical equipment, spacecraft, nuclear reactors, etc. may cause serious harm to human life or have severe social ramifications. As such, any inductors to be used in such equipment may require higher safety and/or reliability considerations and should be clearly differentiated from components used in general purpose applications.</p> <p>◆Operating Current (Verification of Rated current)</p> <p>1. The operating current for inductors must always be lower than their rated values.</p> <p>2. Do not apply current in excess of the rated value because the inductance may be reduced due to the magnetic saturation effect.</p>																																																																																																						
2. PCB Design	<p>◆Pattern configurations (Design of Land-patterns)</p> <p>1. When inductors are mounted on a PCB, the size of land patterns and the amount of solder used (size of fillet) can directly affect inductor performance. Therefore, the following items must be carefully considered in the design of solder land patterns:</p> <p>(1) The amount of solder applied can affect the ability of chips to withstand mechanical stresses which may lead to breaking or cracking. Therefore, when designing land-patterns it is necessary to consider the appropriate size and configuration of the solder pads which in turn determines the amount of solder necessary to form the fillets.</p> <p>(2) When more than one part is jointly soldered onto the same land or pad, the pad must be designed so that each component's soldering point is separated by solder-resist.</p> <p>(3) The larger size of land patterns and amount of solder, the smaller Q value after mounting on PCB. It makes higher the Q value to design land patterns smaller than terminal electrode of chips.</p>	<p>1. The following diagrams and tables show some examples of recommended patterns to prevent excessive solder amounts (larger fillets which extend above the component end terminations). Examples of improper pattern designs are also shown.</p> <p>(1) Recommended land dimensions for a typical chip inductor land patterns for PCBs</p>  <p>Recommended land dimensions for wave-soldering (unit: mm)</p> <table border="1" data-bbox="853 1157 1268 1343"> <thead> <tr> <th>Type</th> <th>1608</th> <th>2125</th> <th>3216</th> </tr> </thead> <tbody> <tr> <td rowspan="2">Size</td> <td>L</td> <td>1.6</td> <td>2.0</td> <td>3.2</td> </tr> <tr> <td>W</td> <td>0.8</td> <td>1.25</td> <td>1.6</td> </tr> <tr> <td>A</td> <td>0.8~1.0</td> <td>1.0~1.4</td> <td>1.8~2.5</td> </tr> <tr> <td>B</td> <td>0.5~0.8</td> <td>0.8~1.5</td> <td>0.8~1.7</td> </tr> <tr> <td>C</td> <td>0.6~0.8</td> <td>0.9~1.2</td> <td>1.2~1.6</td> </tr> </tbody> </table> <p>Recommended land dimensions for reflow-soldering (unit: mm)</p> <table border="1" data-bbox="853 1397 1460 1583"> <thead> <tr> <th>Type</th> <th>0603</th> <th>1005</th> <th>105</th> <th>1608</th> <th>2125</th> <th>3216</th> <th>2520</th> </tr> </thead> <tbody> <tr> <td rowspan="2">Size</td> <td>L</td> <td>0.6</td> <td>1.0</td> <td>1.0</td> <td>1.6</td> <td>2.0</td> <td>3.2</td> <td>2.5</td> </tr> <tr> <td>W</td> <td>0.3</td> <td>0.5</td> <td>0.6</td> <td>0.8</td> <td>1.25</td> <td>1.6</td> <td>2.0</td> </tr> <tr> <td>A</td> <td>0.20~0.30</td> <td>0.45~0.55</td> <td>0.50~0.55</td> <td>0.6~0.8</td> <td>0.8~1.2</td> <td>1.8~2.5</td> <td>1.0~1.4</td> </tr> <tr> <td>B</td> <td>0.20~0.30</td> <td>0.40~0.50</td> <td>0.30~0.40</td> <td>0.6~0.8</td> <td>0.8~1.2</td> <td>0.6~1.5</td> <td>0.6~1.0</td> </tr> <tr> <td>C</td> <td>0.25~0.40</td> <td>0.45~0.55</td> <td>0.60~0.70</td> <td>0.6~0.8</td> <td>0.9~1.6</td> <td>1.2~2.0</td> <td>1.8~2.2</td> </tr> </tbody> </table> <p>Excess solder can affect the ability of chips to withstand mechanical stresses. Therefore, please take proper precautions when designing land-patterns.</p>  <p>Recommended land dimension for Reflow-soldering (unit: mm)</p> <table border="1" data-bbox="1189 1758 1460 1976"> <thead> <tr> <th></th> <th></th> <th>3216</th> <th>2010</th> </tr> </thead> <tbody> <tr> <td rowspan="2">Size</td> <td>L</td> <td>3.2</td> <td>2.0</td> </tr> <tr> <td>W</td> <td>1.6</td> <td>1.0</td> </tr> <tr> <td>a</td> <td></td> <td>0.7~0.9</td> <td>0.5~0.6</td> </tr> <tr> <td>b</td> <td></td> <td>0.8~1.0</td> <td>0.5~0.6</td> </tr> <tr> <td>c</td> <td></td> <td>0.4~0.5</td> <td>0.2~0.3</td> </tr> <tr> <td>d</td> <td></td> <td>0.8</td> <td>0.5</td> </tr> </tbody> </table>	Type	1608	2125	3216	Size	L	1.6	2.0	3.2	W	0.8	1.25	1.6	A	0.8~1.0	1.0~1.4	1.8~2.5	B	0.5~0.8	0.8~1.5	0.8~1.7	C	0.6~0.8	0.9~1.2	1.2~1.6	Type	0603	1005	105	1608	2125	3216	2520	Size	L	0.6	1.0	1.0	1.6	2.0	3.2	2.5	W	0.3	0.5	0.6	0.8	1.25	1.6	2.0	A	0.20~0.30	0.45~0.55	0.50~0.55	0.6~0.8	0.8~1.2	1.8~2.5	1.0~1.4	B	0.20~0.30	0.40~0.50	0.30~0.40	0.6~0.8	0.8~1.2	0.6~1.5	0.6~1.0	C	0.25~0.40	0.45~0.55	0.60~0.70	0.6~0.8	0.9~1.6	1.2~2.0	1.8~2.2			3216	2010	Size	L	3.2	2.0	W	1.6	1.0	a		0.7~0.9	0.5~0.6	b		0.8~1.0	0.5~0.6	c		0.4~0.5	0.2~0.3	d		0.8	0.5
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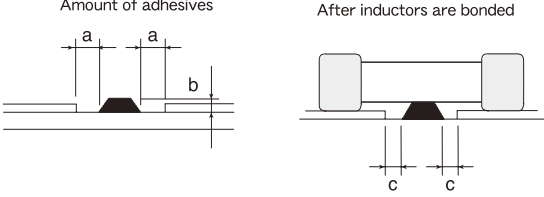
Precautions on the use of Multilayer chip Inductors, Multilayer chip inductors for high frequency, Multilayer ferrite chip beads

Stages	Precautions	Technical considerations																					
2.PCB Design	<p>◆Pattern configurations (Inductor layout on panelized [breakaway] PC boards)</p> <p>1. After inductors have been mounted on the boards, chips can be subjected to mechanical stresses in subsequent manufacturing processes (PCB cutting, board inspection, mounting of additional parts, assembly into the chassis, wave soldering the reflow soldered boards etc.) For this reason, planning pattern configurations and the position of SMD inductors should be carefully performed to minimize stress.</p>	<p>(2) Examples of good and bad solder application</p> <table border="1" data-bbox="852 299 1453 729"> <thead> <tr> <th></th> <th>Not recommended</th> <th>Recommended</th> </tr> </thead> <tbody> <tr> <td>Mixed mounting of SMD and leaded components</td> <td></td> <td></td> </tr> <tr> <td>Component placement close to the chassis</td> <td></td> <td></td> </tr> <tr> <td>Hand-soldering of leaded components near mounted components</td> <td></td> <td></td> </tr> <tr> <td>Horizontal component placement</td> <td></td> <td></td> </tr> </tbody> </table> <p>1-1. The following are examples of good and bad inductor layout; SMD inductors should be located to minimize any possible mechanical stresses from board warp or deflection.</p> <table border="1" data-bbox="852 847 1453 995"> <thead> <tr> <th>Item</th> <th>Not recommended</th> <th>Recommended</th> </tr> </thead> <tbody> <tr> <td>Deflection of the board</td> <td></td> <td></td> </tr> </tbody> </table> <p>1-2. To layout the inductors for the breakaway PC board, it should be noted that the amount of mechanical stresses given will vary depending on inductor layout. An example below should be counted for better design.</p> <div data-bbox="909 1109 1340 1386" data-label="Diagram"> <p>Magnitude of stress $A > B = C > D > E$</p> </div> <p>1-3. When breaking PC boards along their perforations, the amount of mechanical stress on the inductors can vary according to the method used. The following methods are listed in order from least stressful to most stressful: push-back, slit, V-grooving, and perforation. Thus, any ideal SMD inductor layout must also consider the PCB splitting procedure.</p>		Not recommended	Recommended	Mixed mounting of SMD and leaded components			Component placement close to the chassis			Hand-soldering of leaded components near mounted components			Horizontal component placement			Item	Not recommended	Recommended	Deflection of the board		
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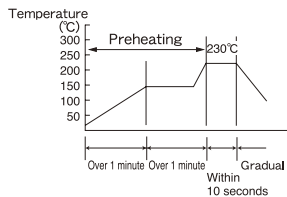
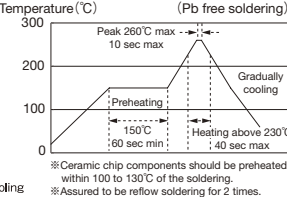
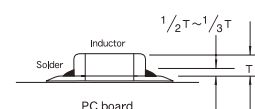
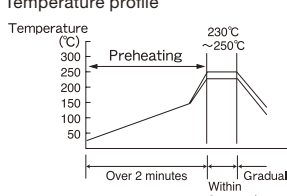
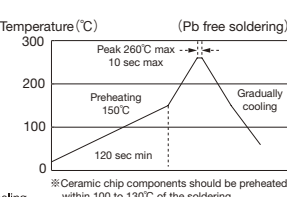
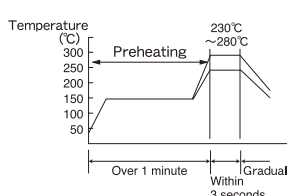
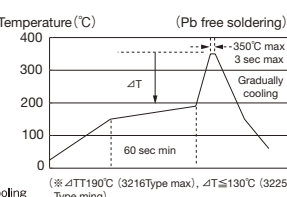
Precautions on the use of Multilayer chip Inductors, Multilayer chip inductors for high frequency, Multilayer ferrite chip beads

Stages	Precautions	Technical considerations									
<p>3.Considerations for automatic placement</p>	<p>◆Adjustment of mounting machine</p> <ol style="list-style-type: none"> Excessive impact load should not be imposed on the inductors when mounting onto the PC boards. The maintenance and inspection of the mounter should be conducted periodically. <p>◆Selection of Adhesives</p> <ol style="list-style-type: none"> Mounting inductors with adhesives in preliminary assembly, before the soldering stage, may lead to degraded inductor characteristics unless the following factors are appropriately checked; the size of land patterns, type of adhesive, amount applied, hardening temperature and hardening period. Therefore, it is imperative to consult the manufacturer of the adhesives on proper usage and amounts of adhesive to use. 	<ol style="list-style-type: none"> If the lower limit of the pick-up nozzle is low, too much force may be imposed on the inductors, causing damage. To avoid this, the following points should be considered before lowering the pick-up nozzle: <ol style="list-style-type: none"> The lower limit of the pick-up nozzle should be adjusted to the surface level of the PC board after correcting for deflection of the board. The pick-up pressure should be adjusted between 1 and 3 N static loads. To reduce the amount of deflection of the board caused by impact of the pick-up nozzle, supporting pins or back-up pins should be used under the PC board. The following diagrams show some typical examples of good pick-up nozzle placement: <table border="1" data-bbox="847 535 1453 805"> <thead> <tr> <th></th> <th>Improper method</th> <th>Proper method</th> </tr> </thead> <tbody> <tr> <td>Single-sided mounting</td> <td></td> <td></td> </tr> <tr> <td>Double-sided mounting</td> <td></td> <td></td> </tr> </tbody> </table> <ol style="list-style-type: none"> As the alignment pin wears out, adjustment of the nozzle height can cause chipping or cracking of the inductors because of mechanical impact on the inductors. To avoid this, the monitoring of the width between the alignment pin in the stopped position, and maintenance, inspection and replacement of the pin should be conducted periodically. Some adhesives may cause reduced insulation resistance. The difference between the shrinkage percentage of the adhesive and that of the inductors may result in stresses on the inductors and lead to cracking. Moreover, too little or too much adhesive applied to the board may adversely affect component placement, so the following precautions should be noted in the application of adhesives. <ol style="list-style-type: none"> Required adhesive characteristics <ol style="list-style-type: none"> The adhesive should be strong enough to hold parts on the board during the mounting & solder process. The adhesive should have sufficient strength at high temperatures. The adhesive should have good coating and thickness consistency. The adhesive should be used during its prescribed shelf life. The adhesive should harden rapidly The adhesive must not be contaminated. The adhesive should have excellent insulation characteristics. The adhesive should not be toxic and have no emission of toxic gasses. 		Improper method	Proper method	Single-sided mounting			Double-sided mounting		
	Improper method	Proper method									
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3.Considerations for automatic placement		<p>When using adhesives to mount inductors on a PCB, inappropriate amounts of adhesive on the board may adversely affect component placement. Too little adhesive may cause the inductors to fall off the board during the solder process. Too much adhesive may cause defective soldering due excessive flow of adhesive on to the land or solder pad.</p> <p>[Recommended conditions]</p> <table border="1" data-bbox="898 447 1452 567"> <thead> <tr> <th>Figure</th> <th>0805 case sizes as examples</th> </tr> </thead> <tbody> <tr> <td>a</td> <td>0.3mm min</td> </tr> <tr> <td>b</td> <td>100 ~120 μm</td> </tr> <tr> <td>c</td> <td>Area with no adhesive</td> </tr> </tbody> </table> 	Figure	0805 case sizes as examples	a	0.3mm min	b	100 ~120 μm	c	Area with no adhesive
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4.Soldering	<p>◆Selection of Flux</p> <ol style="list-style-type: none"> Since flux may have a significant effect on the performance of inductors, it is necessary to verify the following conditions prior to use; <ol style="list-style-type: none"> Flux used should be with less than or equal to 0.1 wt% (Chlorine conversion method) of halogenated content. Flux having a strong acidity content should not be applied. When soldering inductors on the board, the amount of flux applied should be controlled at the optimum level. When using water-soluble flux, special care should be taken to properly clean the boards. <p>◆Soldering</p> <p>Temperature, time, amount of solder, etc. are specified in accordance with the following recommended conditions.</p>	<ol style="list-style-type: none"> When too much halogenated substance (Chlorine, etc.) content is used to activate the flux, or highly acidic flux is used, an excessive amount of residue after soldering may lead to corrosion of the terminal electrodes or degradation of insulation resistance on the surface of the Inductor. <ol style="list-style-type: none"> Flux is used to increase solderability in flow soldering, but if too much is applied, a large amount of flux gas may be emitted and may detrimentally affect solderability. To minimize the amount of flux applied, it is recommended to use a flux-bubbling system. Since the residue of water-soluble flux is easily dissolved by water content in the air, the residue on the surface of Inductor in high humidity conditions may cause a degradation of insulation resistance and therefore affect the reliability of the components. The cleaning methods and the capability of the machines used should also be considered carefully when selecting water-soluble flux. Preheating when soldering <p>Heating: Chip inductor components should be preheated to within 100 to 130°C of the soldering. Cooling: The temperature difference between the components and cleaning process should not be greater than 100 °C.</p> <p>Chip inductors are susceptible to thermal shock when exposed to rapid or concentrated heating or rapid cooling. Therefore, the soldering process must be conducted with a great care so as to prevent malfunction of the components due to excessive thermal shock.</p> 								

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Stages	Precautions	Technical considerations
4.Soldering	<p>◆And please contact us about peak temperature when you use lead-free paste.</p>	<p>Recommended conditions for soldering</p> <p>[Reflow soldering]</p> <p>Temperature profile</p>   <p>Caution</p> <ol style="list-style-type: none"> The ideal condition is to have solder mass (fillet) controlled to 1/2 to 1/3 of the thickness of the inductor, as shown below:  <ol style="list-style-type: none"> Because excessive dwell times can detrimentally affect solderability, soldering duration should be kept as close to recommended times as possible. <p>[Wave soldering]</p> <p>Temperature profile</p>   <p>Caution</p> <ol style="list-style-type: none"> Make sure the inductors are preheated sufficiently. The temperature difference between the inductor and melted solder should not be greater than 100 to 130°C Cooling after soldering should be as gradual as possible. Wave soldering must not be applied to the inductors designated as for reflow soldering only. <p>[Hand soldering]</p> <p>Temperature profile</p>   <p>Caution</p> <ol style="list-style-type: none"> Use a 20W soldering iron with a maximum tip diameter of 1.0 mm. The soldering iron should not directly touch the inductor.
5.Cleaning	<p>◆Cleaning conditions</p> <ol style="list-style-type: none"> When cleaning the PC board after the Inductors are all mounted, select the appropriate cleaning solution according to the type of flux used and purpose of the cleaning (e.g. to remove soldering flux or other materials from the production process.) 	<ol style="list-style-type: none"> The use of inappropriate solutions can cause foreign substances such as flux residue to adhere to the inductor, resulting in a degradation of the inductor's electrical properties (especially insulation resistance).

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Stages	Precautions	Technical considerations						
5.Cleaning	<p>2. Cleaning conditions should be determined after verifying, through a test run, that the cleaning process does not affect the inductor's characteristics.</p>	<p>2. Inappropriate cleaning conditions (insufficient or excessive cleaning) may detrimentally affect the performance of the inductors.</p> <p>(1) Excessive cleaning In the case of ultrasonic cleaning, too much power output can cause excessive vibration of the PC board which may lead to the cracking of the inductor or the soldered portion, or decrease the terminal electrodes' strength. Thus the following conditions should be carefully checked;</p> <table border="0" style="width: 100%;"> <tr> <td style="padding-right: 20px;">Ultrasonic output</td> <td>Below 20 w/ℓ</td> </tr> <tr> <td>Ultrasonic frequency</td> <td>Below 40 kHz</td> </tr> <tr> <td>Ultrasonic washing period</td> <td>5 min. or less</td> </tr> </table>	Ultrasonic output	Below 20 w/ℓ	Ultrasonic frequency	Below 40 kHz	Ultrasonic washing period	5 min. or less
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6. Post cleaning processes	<p>◆Application of resin coatings, moldings, etc. to the PCB and components.</p> <p>1. With some type of resins a decomposition gas or chemical reaction vapor may remain inside the resin during the hardening period or while left under normal storage conditions resulting in the deterioration of the inductor's performance.</p> <p>2. When a resin's hardening temperature is higher than the inductor's operating temperature, the stresses generated by the excess heat may lead to inductor damage or destruction.</p> <p>3. Stress caused by a resin's temperature generated expansion and contraction may damage inductors.</p> <p>The use of such resins, molding materials etc. is not recommended.</p>							
7. Handling	<p>◆Breakaway PC boards (splitting along perforations)</p> <p>1. When splitting the PC board after mounting inductors and other components, care is required so as not to give any stresses of deflection or twisting to the board.</p> <p>2. Board separation should not be done manually, but by using the appropriate devices.</p> <p>◆General handling precautions</p> <ol style="list-style-type: none"> 1. Always wear static control bands to protect against ESD. 2. Keep the inductors away from all magnets and magnetic objects. 3. Use non-magnetic tweezers when handling inductors. 4. Any devices used with the inductors (soldering irons, measuring instruments) should be properly grounded. 5. Keep bare hands and metal products (i.e., metal desk) away from chip electrodes or conductive areas that lead to chip electrodes. 6. Keep inductors away from items that generate magnetic fields such as speakers or coils. <p>◆Mechanical considerations</p> <ol style="list-style-type: none"> 1. Be careful not to subject the inductors to excessive mechanical shocks. <ol style="list-style-type: none"> (1) If inductors are dropped on the floor or a hard surface they should not be used. (2) When handling the mounted boards, be careful that the mounted components do not come in contact with or bump against other boards or components. 							

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Stages	Precautions	Technical considerations				
8. Storage conditions	<p>◆Storage</p> <p>1. To maintain the solderability of terminal electrodes and to keep the packaging material in good condition, care must be taken to control temperature and humidity in the storage area. Humidity should especially be kept as low as possible.</p> <p>Recommended conditions</p> <table border="0"> <tr> <td>Ambient temperature</td> <td>Below 40 °C</td> </tr> <tr> <td>Humidity</td> <td>Below 70% RH</td> </tr> </table> <p>The ambient temperature must be kept below 30 °C. Even under ideal storage conditions inductor electrode solderability decreases as time passes, so inductors should be used within 6 months from the time of delivery.</p> <p>*The packaging material should be kept where no chlorine or sulfur exists in the air.</p>	Ambient temperature	Below 40 °C	Humidity	Below 70% RH	<p>1. If the parts are stocked in a high temperature and humidity environment, problems such as reduced solderability caused by oxidation of terminal electrodes and deterioration of taping/packaging materials may take place. For this reason, components should be used within 6 months from the time of delivery. If exceeding the above period, please check solderability before using the inductors</p>
Ambient temperature	Below 40 °C					
Humidity	Below 70% RH					